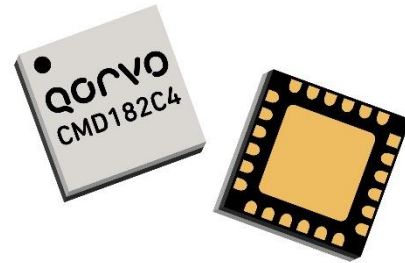
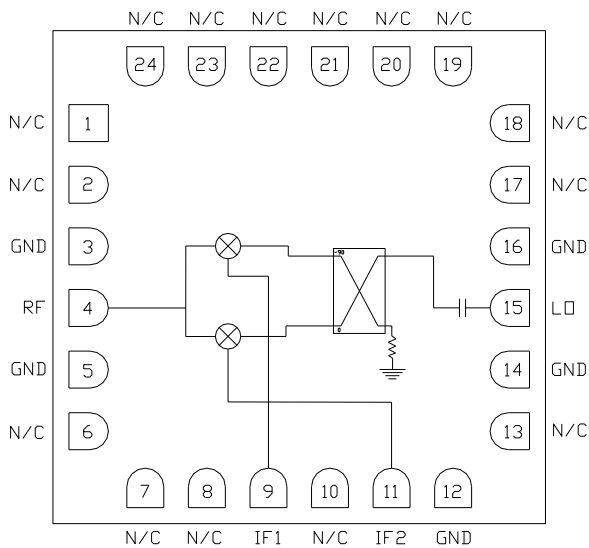


### Product Overview

The CMD182C4 is a compact I/Q mixer in a leadless surface mount package that can be used as either an image reject mixer or a single sideband upconverter. The CMD182C4 utilizes two double balanced mixer cells and a 90 degree hybrid. An external IF hybrid is needed to complete the image rejection. The CMD182C4 is a much smaller alternative to higher cost hybrid image reject mixers and single sideband upconverter assemblies.



### Functional Block Diagram



### Key Features

- Low Conversion Loss
- High Isolation
- Image Rejection: 30 dB
- Wide IF Bandwidth
- Pb-Free RoHs Compliant 4x4 mm SMT Package

### Ordering Information

Part No.	Description
CMD182C4	6-10 GHz I/Q Mixer, 100 Piece 7" Reel
CMD182C4-EVB	Evaluation Board

### Electrical Performance (IF = 100 MHz, LO = +15 dBm, T<sub>A</sub> = 25 °C, F = 8 GHz)

Parameter	Min	Typ	Max	Units
Frequency Range, RF & LO		6 - 10		GHz
Frequency Range, IF	DC		3.5	GHz
Conversion Loss (as IRM)		6		dB
Image Rejection		30		dB
LO to RF Isolation		46		dB
LO to IF Isolation		20		dB
Input P1dB		9		dBm

Unless otherwise noted, all measurements performed as a downconverter, IF = 100 MHz

## Absolute Maximum Ratings

Parameter	Rating
RF / IF Input Power	+25 dBm
LO Drive	+25 dBm
Operating Temperature	-40 to 85 °C
Storage Temperature	-55 to 150 °C
Thermal Resistance, $\theta_{JC}$	124.25 °C/W
Power Dissipation, $P_{diss}$	347 mW

Exceeding any one or combination of the maximum ratings may cause permanent damage to the device.

## Electrical Specifications (IF = 100 MHz, LO = +15 dBm, $T_A = 25\text{ °C}$ )

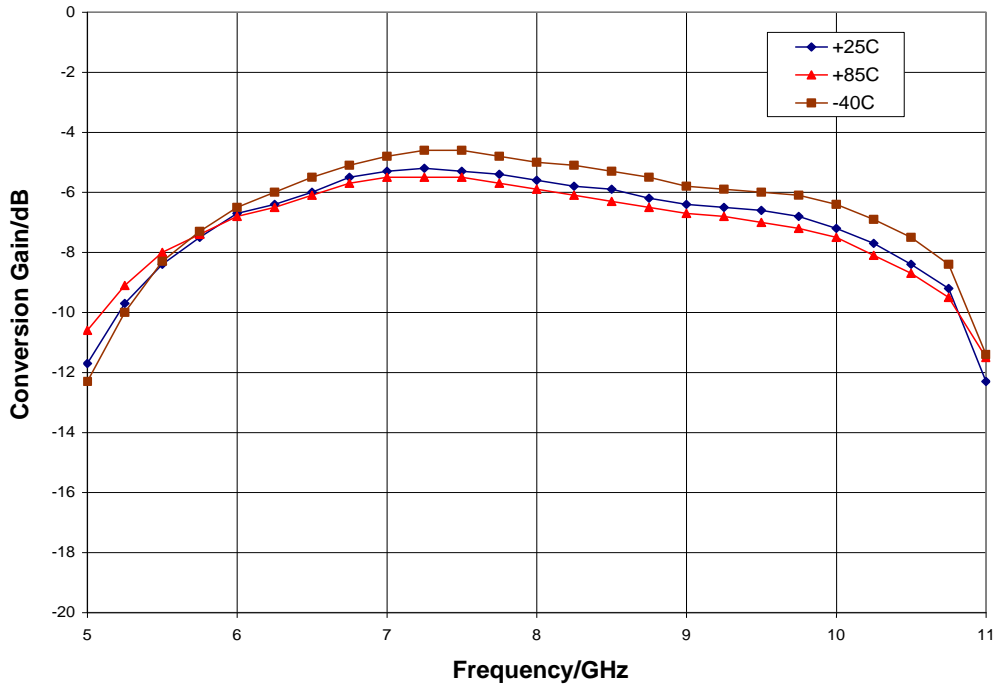
Parameter	Min	Typ	Max	Min	Typ	Max	Units
Frequency Range, RF & LO		6 - 10			7.1 - 8.5		GHz
Frequency Range, IF	DC		3.5	DC		3.5	GHz
Conversion Loss (as IRM)		6	9		5.5	8	dB
Image Rejection	20	28		25	30		dB
LO to RF Isolation	39	50		39	50		dB
LO to IF Isolation	15	20		15	20		dB
Input P1dB		9			9		dBm
Input IP3		18			17.5		dBm

Unless otherwise noted, all measurements performed as a downconverter, IF = 100 MHz

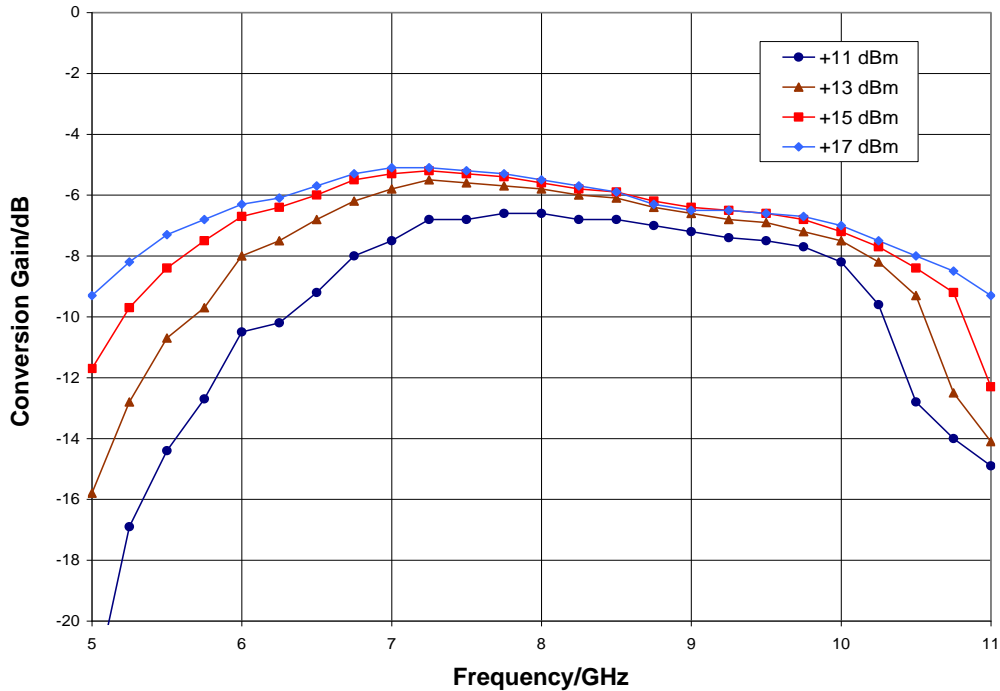
Typical Performance

Data Taken as IRM with External IF Hybrid

Conversion Gain vs. Temperature, LO = +15 dBm, IF = 100 MHz USB



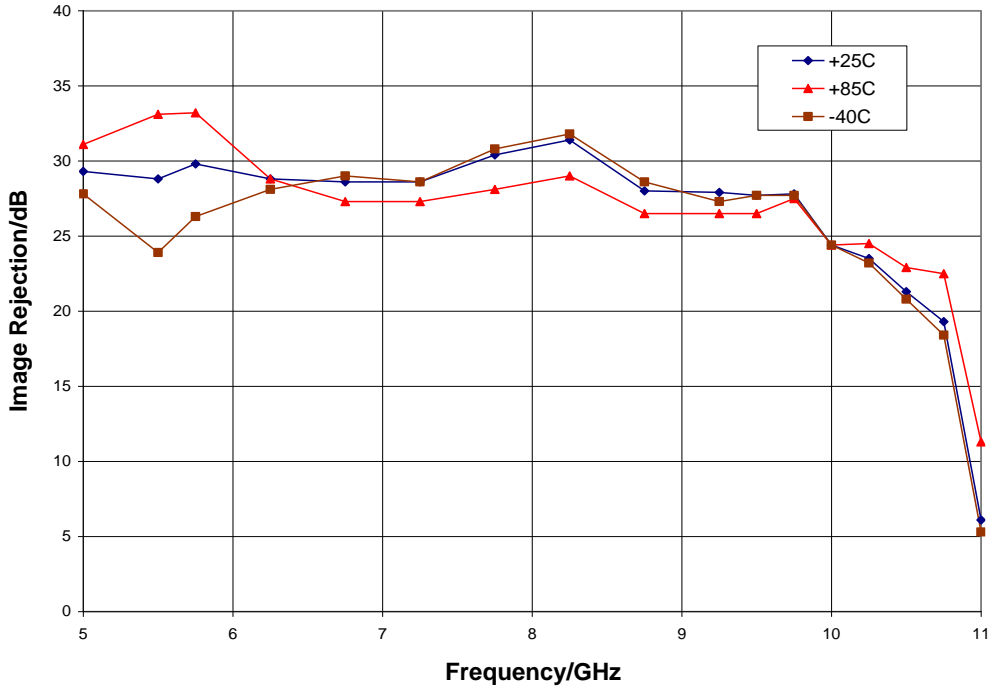
Conversion Gain vs. LO Drive, IF = 100 MHz USB



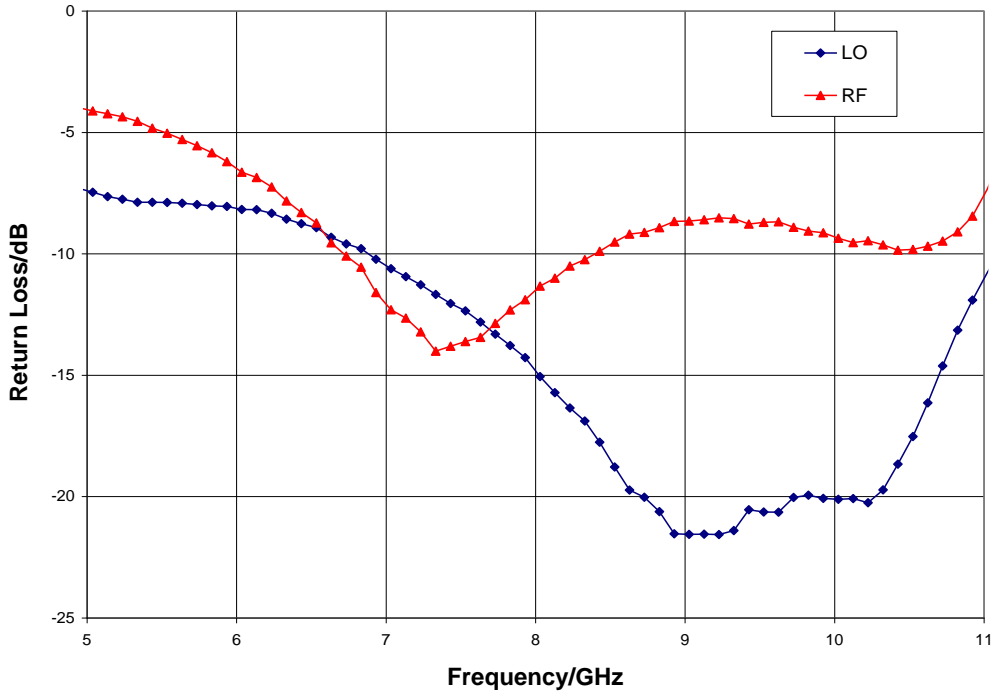
Typical Performance

Data Taken as IRM with External IF Hybrid

Image Rejection, LO = +15 dBm, IF = 100 MHz USB



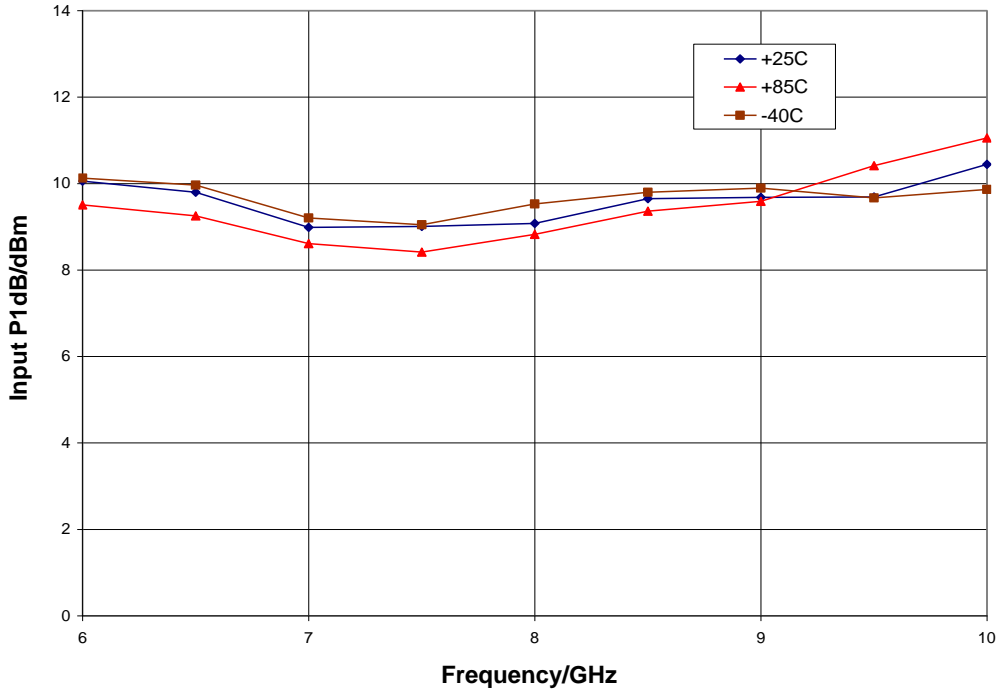
Return Loss, LO = +15 dBm



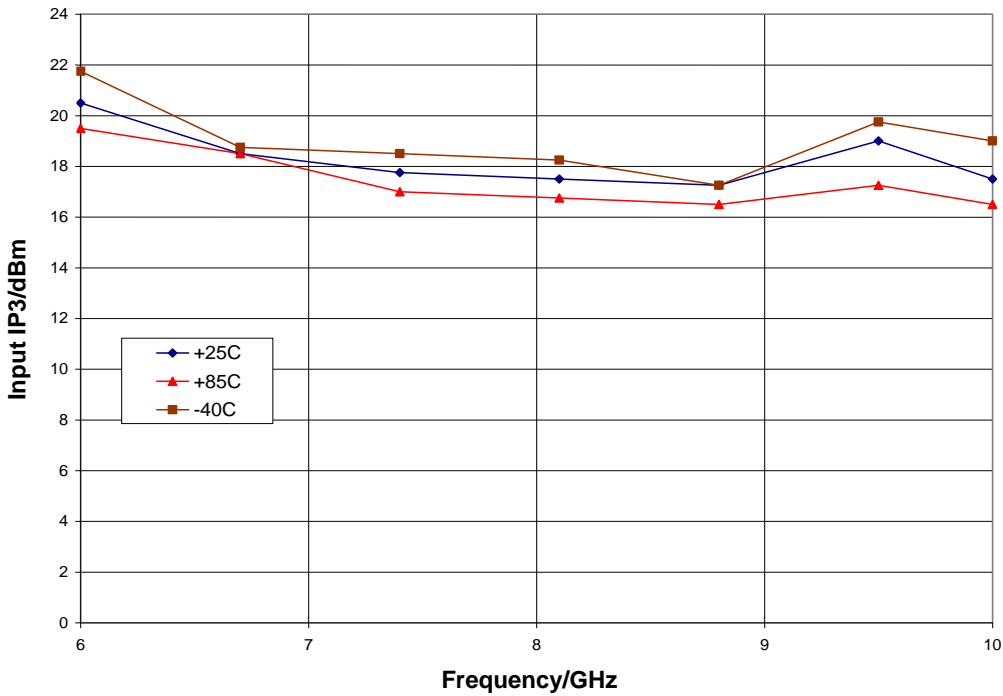
Typical Performance

Data Taken as IRM with External IF Hybrid

Input P1dB vs. Temperature, LO = +15 dBm, IF = 100 MHz USB



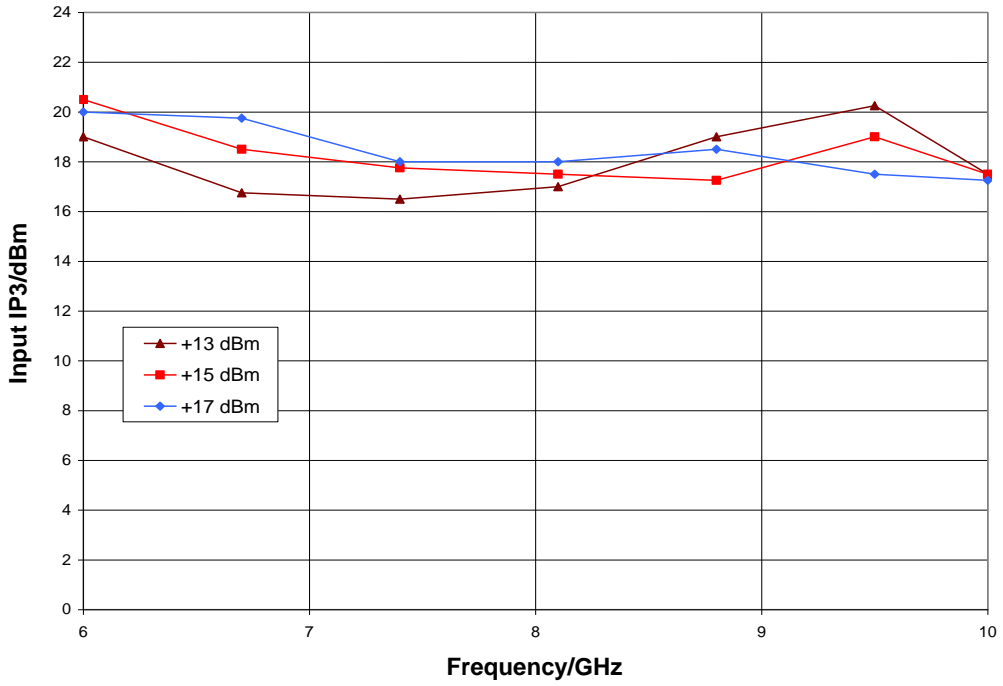
Input IP3 vs. Temperature, LO = +15 dBm, IF = 100 MHz USB



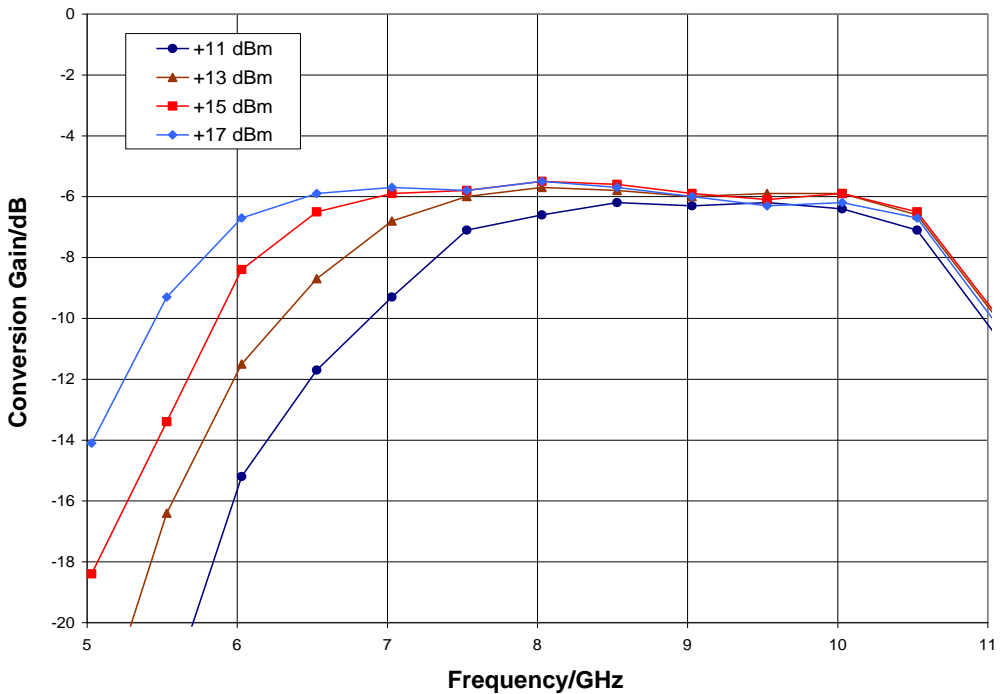
Typical Performance

Data taken As IRM With External IF Hybrid

Input IP3 vs. LO Drive, IF = 100 MHz USB



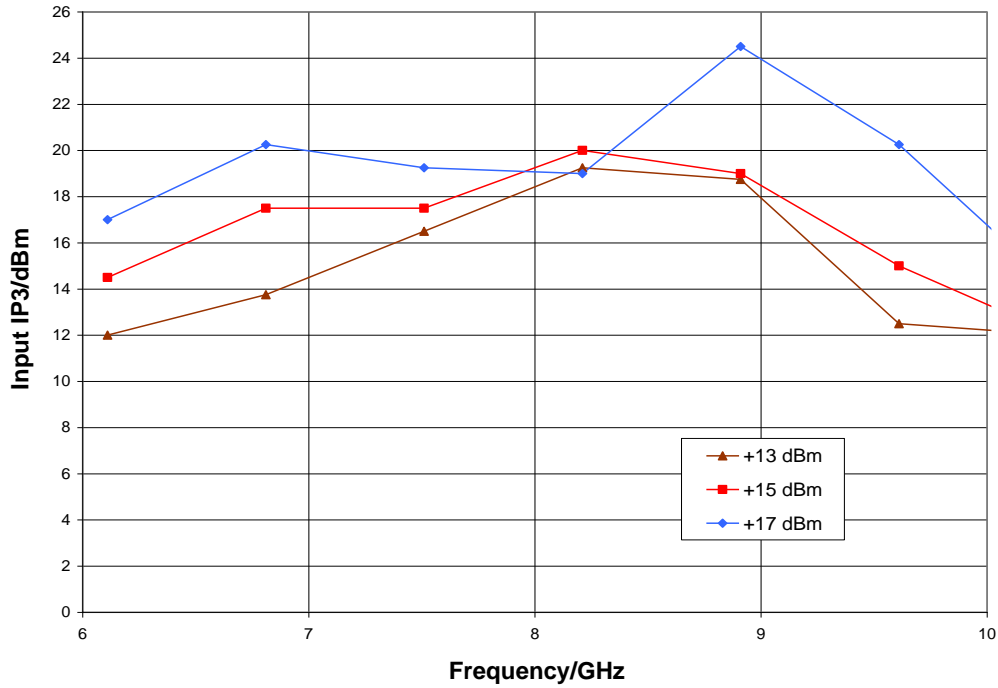
Upconverter Performance, Conversion Gain vs. LO Drive, IF = 1 GHz



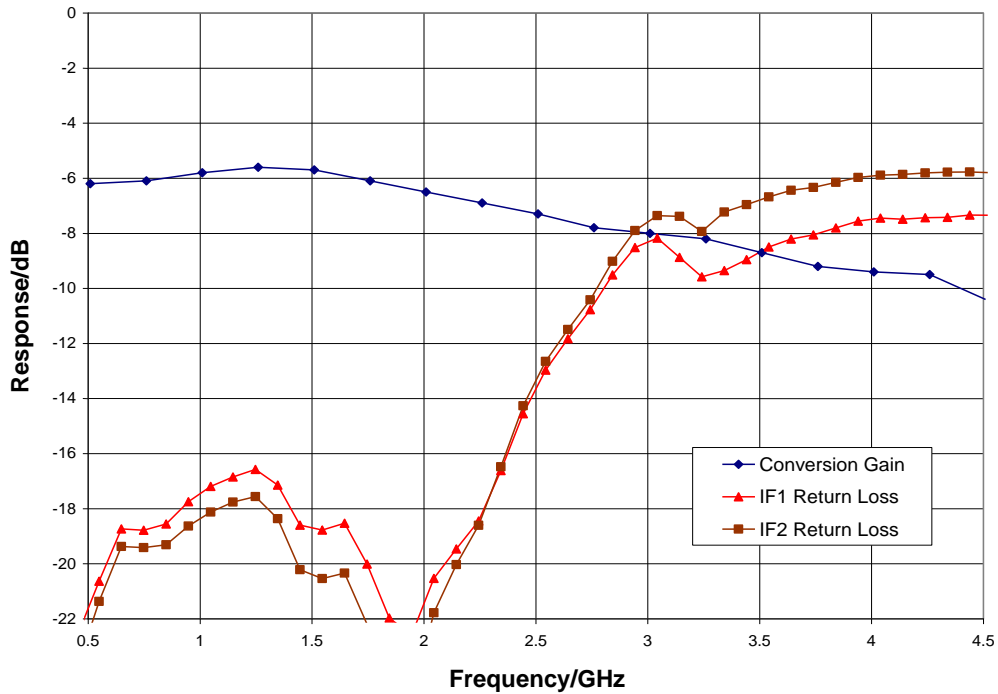
Typical Performance

Data Taken as IRM With External IF Hybrid

Upconverter Performance, Input IP3 vs. LO Drive, IF = 500 MHz

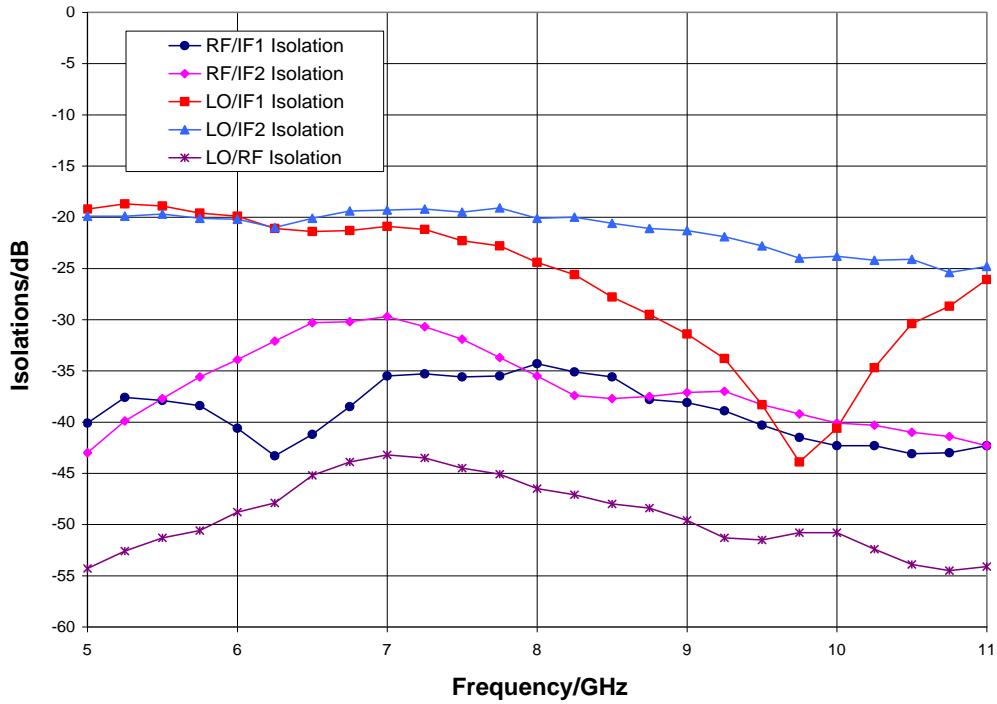


IF Bandwidth, LO = +15 dBm, Return Loss Data Taken Without IF Hybrid



Typical Performance

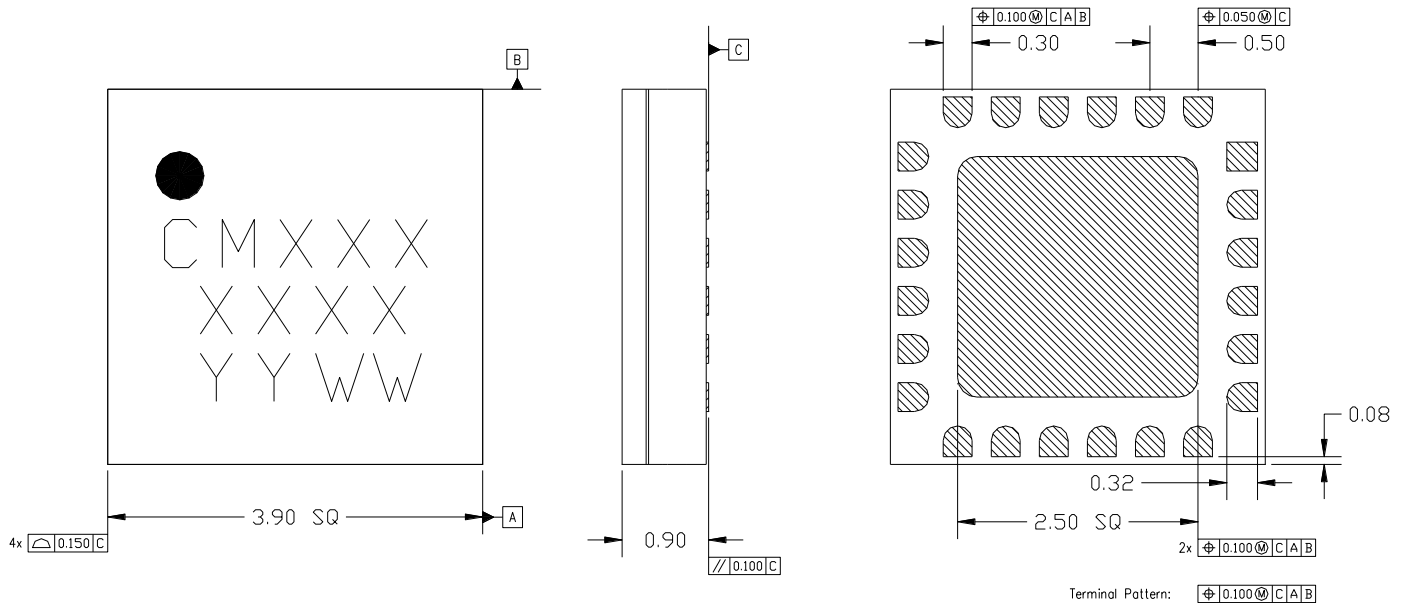
Isolation, LO = +15 dBm. Data Taken Without IF Hybrid





## Mechanical Information

### Package Information and Dimensions



#### Notes:

- All dimensions shown in mm.
- Material: Black alumina
- Lead finish
  - Ni: 8.89um max, 1.27um min
  - Pd: 0.17um max, 0.07um min
  - Au: 0.254um max, 0.03um min
- Marking
  - Line 1: Part number
    - Example: CMD182C4 shall be marked as CM182
  - Line 2: Lot number
  - Line 3: Date code – Last 2 digits of the year of manufacture followed by a 2 digit week code
- Alternate pin #1 identifier is a single square pad
- Alternate die paddle may have chamfered corners

### Recommended PCB Land Pattern

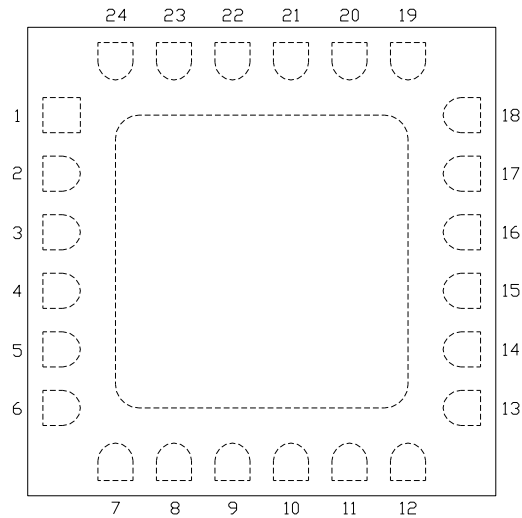
Qorvo recommends that the user develop the land pattern that will provide the best design for proper solder reflow and device attach for their specific application. Please review Qorvo Application Note AN 105 for a recommended land pattern approach.

### Recommended Solder Reflow Profile

Qorvo recommends screen printing with belt furnace reflow to ensure proper solder reflow and device attach. Please review Qorvo Application Note AN 102 for a recommended solder reflow profile.

## Pin Description

### Pin Diagram



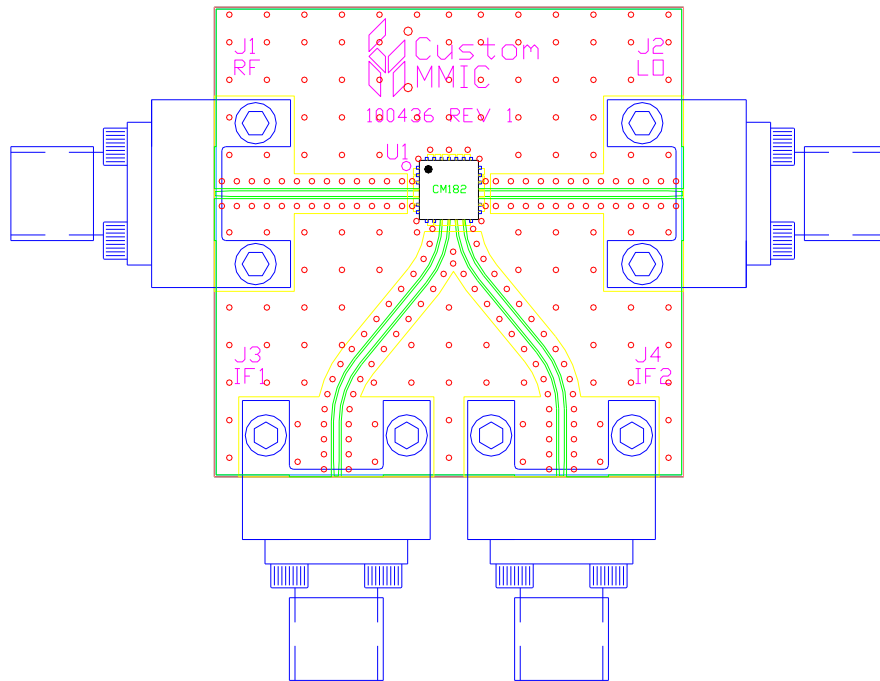
### Functional Description

Pin	Function	Description	Schematic
1, 2, 6 - 8, 10, 13, 17 - 24	N/C	No connection required These pins may be connected to RF / DC ground	
3, 5, 12, 14, 16 and die paddle	Ground	Connect to RF / DC ground	
4	RF	This pin is DC coupled and matched to 50 ohms	
9	IF1	This pin is DC coupled. For applications not requiring operation to DC, this port should be DC blocked externally using a series capacitor whose value has been chosen to pass the necessary IF frequency range. For operation to DC, this pin must not source or sink more than 16 mA of current or part non-function or part failure may result.	
11	IF2		
15	LO	This pin is AC coupled and matched to 50 ohms	

**Applications Information**

**Evaluation Board**

The circuit board shown has been developed for optimized assembly at Qorvo. A sufficient number of via holes should be used to connect the top and bottom ground planes. As surface mount processes vary, careful process development is recommended.



**Bill of Material**

Designator	Value	Description
J1 - J4		SMA End Launch Connector
U1		CMD182C4 I/Q Mixer
PCB		100436 Evaluation PCB

**GaAs MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.**

## Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 1A	ESDA / JEDEC JS-001-2012
MSL – Moisture Sensitivity Level	Level 1	JEDEC standard IPC/JEDEC J-STD-020



Caution!  
 ESD-Sensitive Device

## RoHS Compliance

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Antimony Free
- TBBP-A (C<sub>15</sub>H<sub>12</sub>Br<sub>4</sub>O<sub>2</sub>) Free
- SVHC Free
- Halogen Free
- PFOS Free

## Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

**Web:** [www.qorvo.com](http://www.qorvo.com)

**Tel:** 1-844-890-8163

**Email:** [customer.support@qorvo.com](mailto:customer.support@qorvo.com)

## Important Notice

The information contained in this Data Sheet and any associated documents (“Data Sheet Information”) is believed to be reliable; however, Qorvo makes no warranties regarding the Data Sheet Information and assumes no responsibility or liability whatsoever for the use of said information. All Data Sheet Information is subject to change without notice. Customers should obtain and verify the latest relevant Data Sheet Information before placing orders for Qorvo® products. Data Sheet Information or the use thereof does not grant, explicitly, implicitly or otherwise any rights or licenses to any third party with respect to patents or any other intellectual property whether with regard to such Data Sheet Information itself or anything described by such information.

DATA SHEET INFORMATION DOES NOT CONSTITUTE A WARRANTY WITH RESPECT TO THE PRODUCTS DESCRIBED HEREIN, AND QORVO HEREBY DISCLAIMS ANY AND ALL WARRANTIES WITH RESPECT TO SUCH PRODUCTS WHETHER EXPRESS OR IMPLIED BY LAW, COURSE OF DEALING, COURSE OF PERFORMANCE, USAGE OF TRADE OR OTHERWISE, INCLUDING THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. Without limiting the generality of the foregoing, Qorvo® products are not warranted or authorized for use as critical components in medical, life-saving, or life-sustaining applications, or other applications where a failure would reasonably be expected to cause severe personal injury or death. Applications described in the Data Sheet Information are for illustrative purposes only. Customers are responsible for validating that a particular product described in the Data Sheet Information is suitable for use in a particular application.

© 2022 Qorvo US, Inc. All rights reserved. This document is subject to copyright laws in various jurisdictions worldwide and may not be reproduced or distributed, in whole or in part, without the express written consent of Qorvo US, Inc. | QORVO® is a registered trademark of Qorvo US, Inc.